

08207

FIG. 1

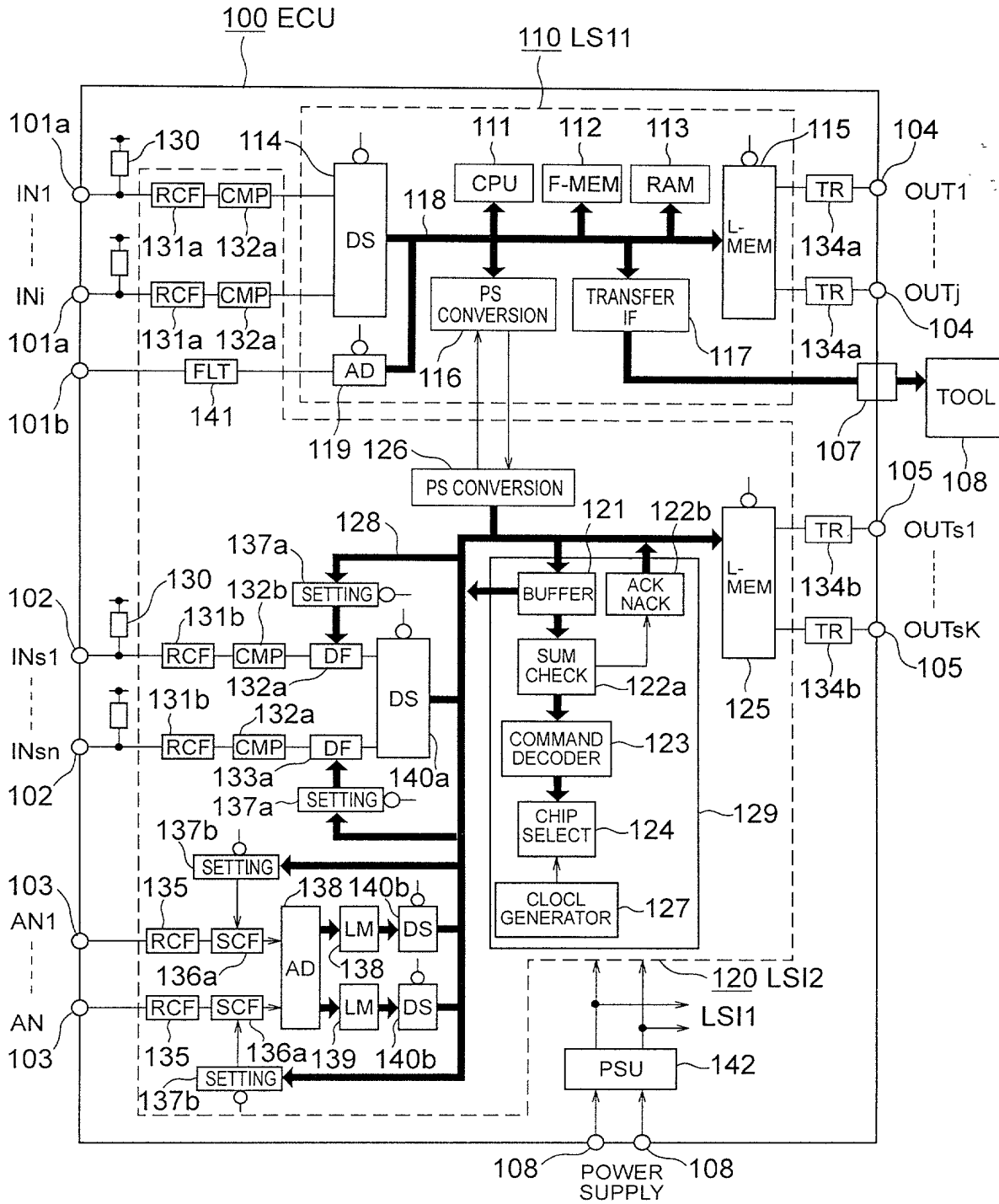
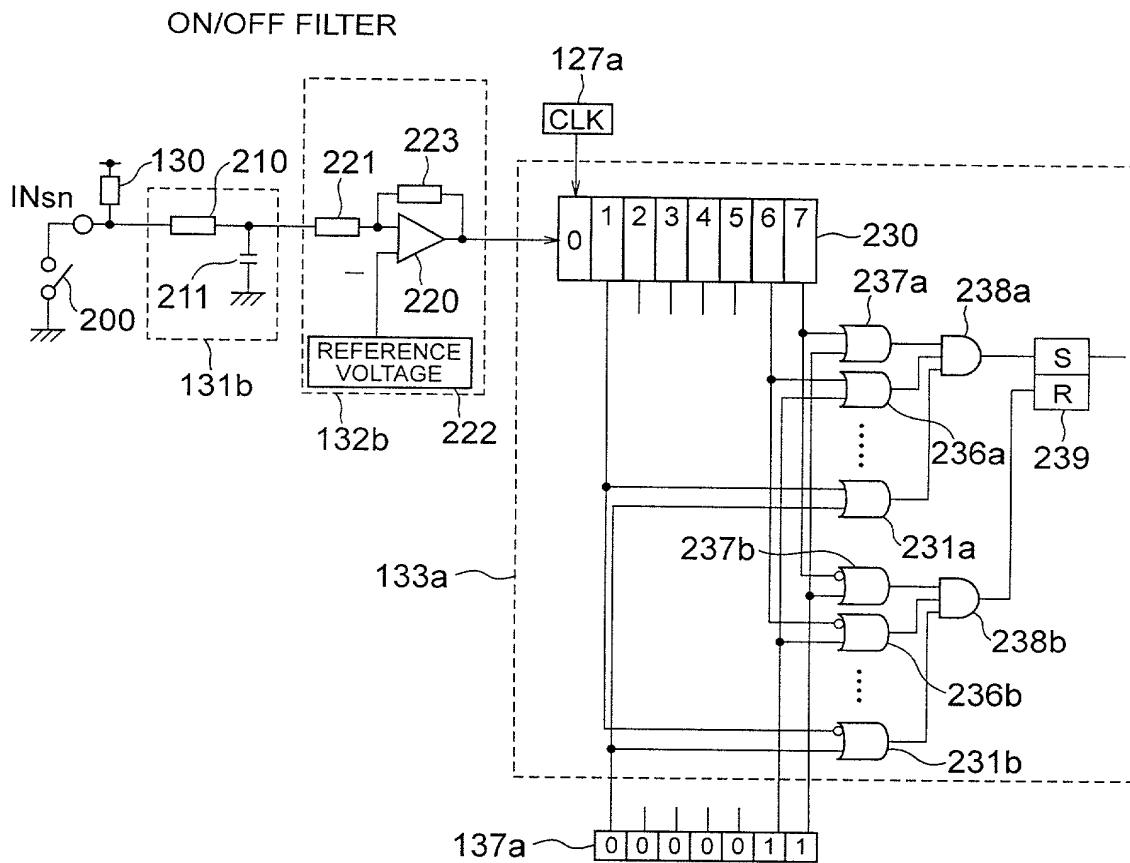


FIG. 2



[illegible]

FIG. 4A

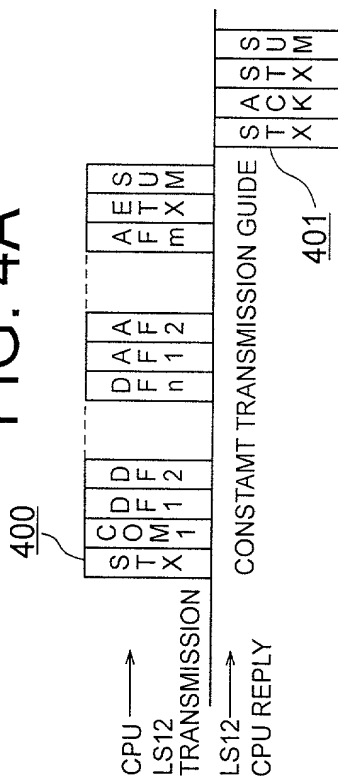


FIG. 4B

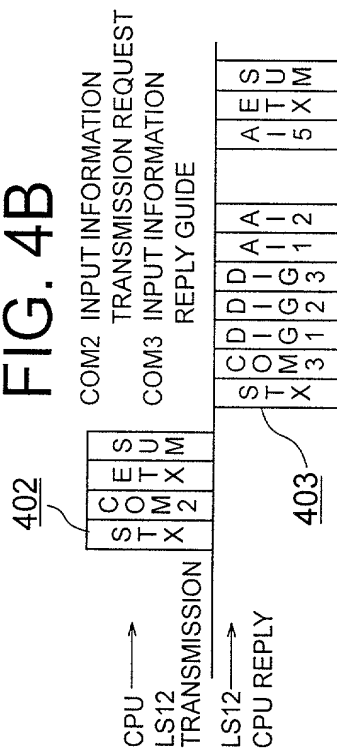


FIG. 4C

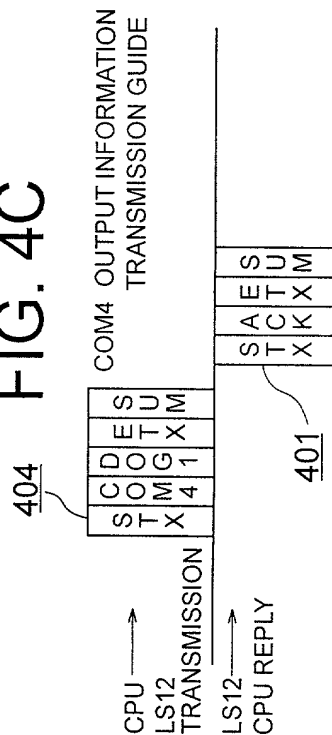


FIG. 4D

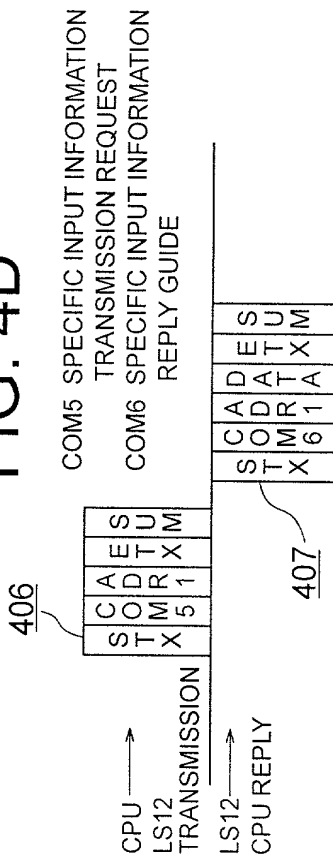


FIG. 4E

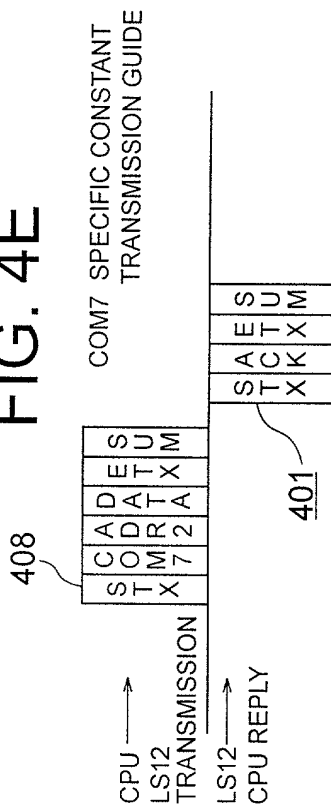


FIG. 4F

COM1	10H
COM2	20H~23H
COM3	30H~33H
COM4	40H
COM5	50H
COM6	60H
COM7	70H

STX	55H
ETX	AAH
ACK	81H
NACK	82H

FIG. 5

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graph TD
    STRAIT500([STRAIT 500]) --> INIT501{INITIAL OPERATION 501}
    INIT501 -- NO --> END([END])
    INIT501 -- YES --> TRANSFER502[TRANSFER F-MEM → RAM 502]
    TRANSFER502 --> COM1_503[TRANSMIT COM1 503]
    COM1_503 --> ACT504[ACTIVATE MONITORING TIMER 504]
    ACT504 --> ACK505{ACK RECEIVED? 505}
    ACK505 -- YES --> REG506[RECEIVED DATA CONSTANT SETTING REGISTER 506]
    ACK505 -- NO --> COM1_503
    REG506 --> END
    
    COM1_503 --> COM2_503
    COM2_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM2_503
    
    COM2_503 --> COM3_503
    COM3_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM3_503
    
    COM3_503 --> COM4_503
    COM4_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM4_503
    
    COM4_503 --> COM5_503
    COM5_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM5_503
    
    COM5_503 --> COM6_503
    COM6_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM6_503
    
    COM6_503 --> COM7_503
    COM7_503 --> ACT504
    ACT504 --> ACK505
    ACK505 -- YES --> REG506
    ACK505 -- NO --> COM7_503
    
    COM7_503 --> COR510{LEARNING CORRECTION? 510}
    COR510 -- NO --> END
    COR510 -- YES --> TRANSFER511[TRANSFER F-MEM → RAM 511]
    TRANSFER511 --> COM1_511[TRANSMIT COM1 511]
    COM1_511 --> ACT512[ACTIVATE MONITORING TIMER 512]
    ACT512 --> ACK513{ACK RECEIVED? 513}
    ACK513 -- YES --> REG514[RECEIVED DATA CONSTANT SETTING REGISTER 514]
    ACK513 -- NO --> COM1_511
    REG514 --> END
    
    COR510 --> COR520{SPECIFIC INPUT NECESSARY? 520}
    COR520 -- NO --> END
    COR520 -- YES --> TRANSFER521[TRANSFER F-MEM → RAM 521]
    TRANSFER521 --> COM1_521[TRANSMIT COM1 521]
    COM1_521 --> ACT522[ACTIVATE MONITORING TIMER 522]
    ACT522 --> ACK523{ACK RECEIVED? 523}
    ACK523 -- YES --> REG524[RECEIVED DATA CONSTANT SETTING REGISTER 524]
    ACK523 -- NO --> COM1_521
    REG524 --> END
    
    COR520 --> COR530{FLAG0 SET? 530}
    COR530 -- NO --> END
    COR530 -- YES --> TRANSFER531[TRANSFER F-MEM → RAM 531]
    TRANSFER531 --> COM1_531[TRANSMIT COM1 531]
    COM1_531 --> ACT532[ACTIVATE MONITORING TIMER 532]
    ACT532 --> ACK533{ACK RECEIVED? 533}
    ACK533 -- YES --> REG534[RECEIVED DATA CONSTANT SETTING REGISTER 534]
    ACK533 -- NO --> COM1_531
    REG534 --> END
    
    COR530 --> COR560{FLAG3 SET? 560}
    COR560 -- NO --> END
    COR560 -- YES --> TRANSFER561[TRANSFER F-MEM → RAM 561]
    TRANSFER561 --> COM1_561[TRANSMIT COM1 561]
    COM1_561 --> ACT562[ACTIVATE MONITORING TIMER 562]
    ACT562 --> ACK563a{ACK RECEIVED? 563a}
    ACK563a -- YES --> REG564[RECEIVED DATA CONSTANT SETTING REGISTER 564]
    ACK563a -- NO --> COM1_561
    REG564 --> END
    
    COR560 --> COR570{RECEIVED DATA → LACH MEMORY 570}
    COR570 -- YES --> END
    COR570 -- NO --> END
  
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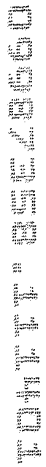

[illegible]

FIG. 8

